

# Si AFSID Wafer 14 Chip 2 2010-07-12 mounted on quartz carrier

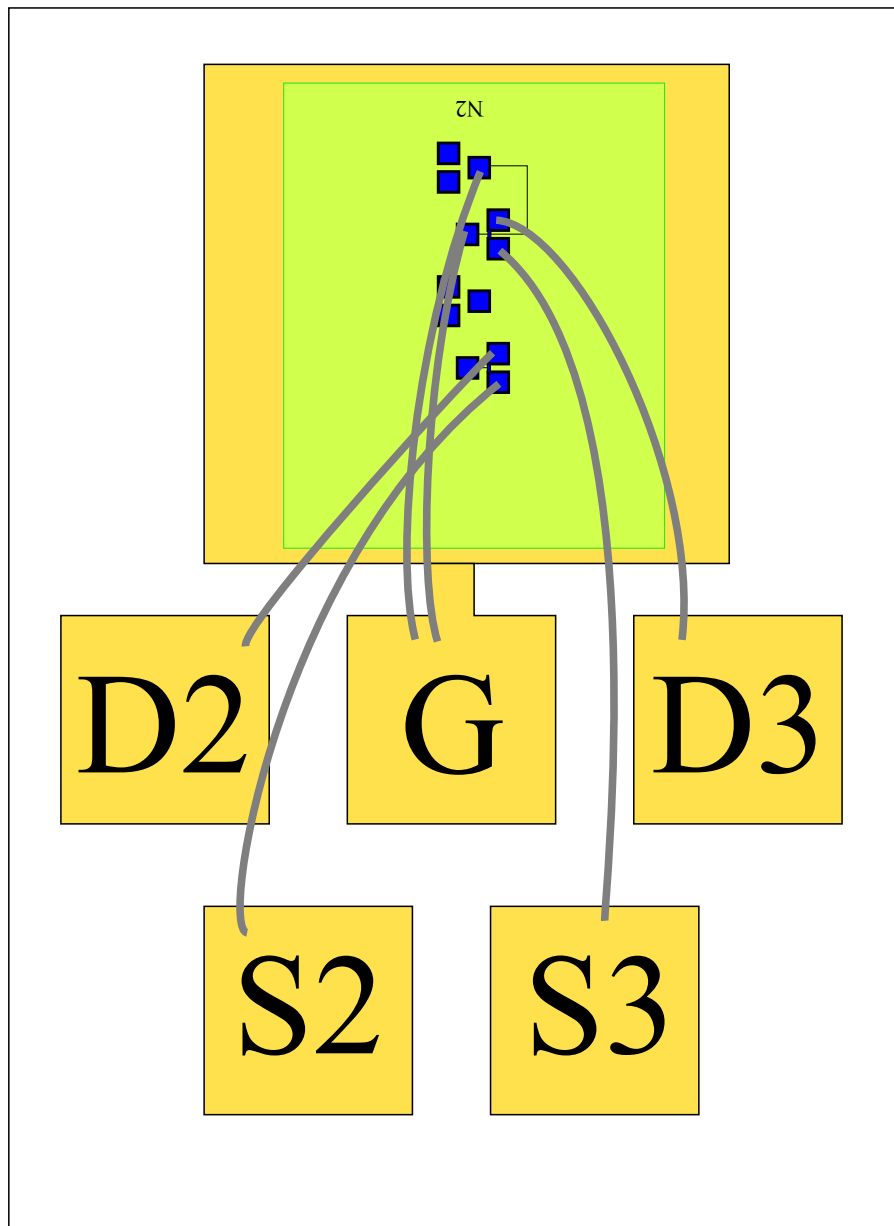


Figure 1: A sketch of the quartz-carrier device. The two source-drain devices are labelled - S for source and D for drain. The gate on device 3 is shorted out with the channel. Resistance of the two channels is about  $6\text{M}\Omega$ .